

Figure 1 (Prior Art)

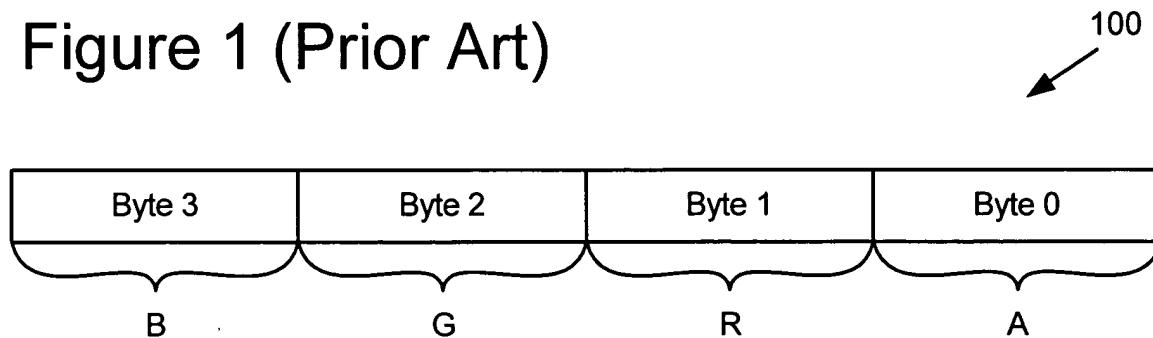


Figure 2 (Prior Art)

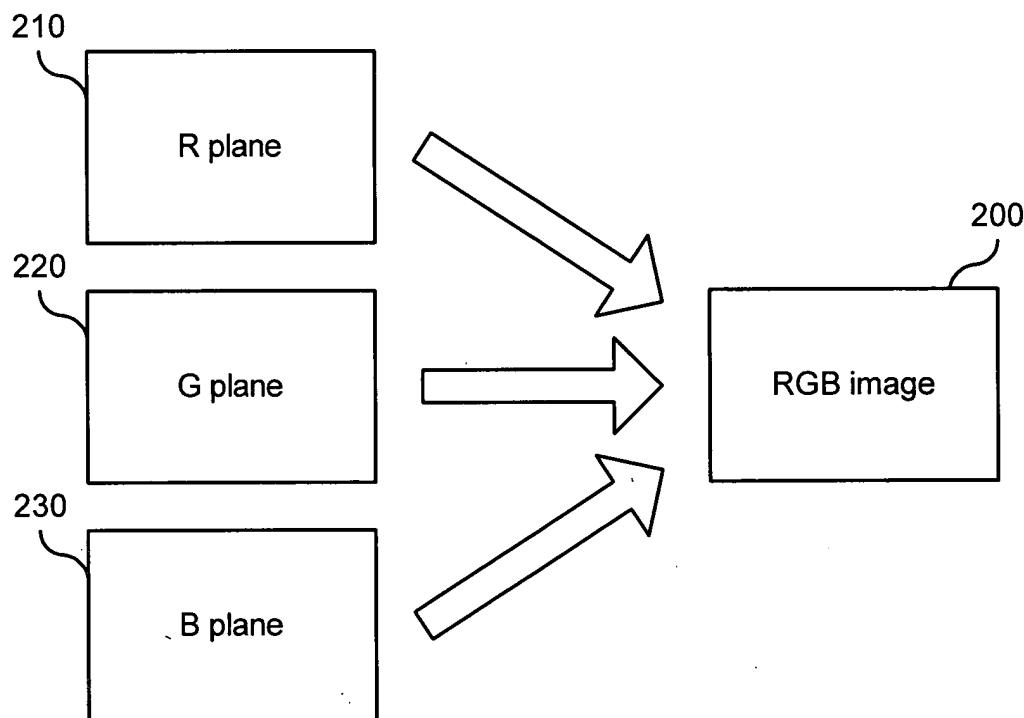


Figure 3

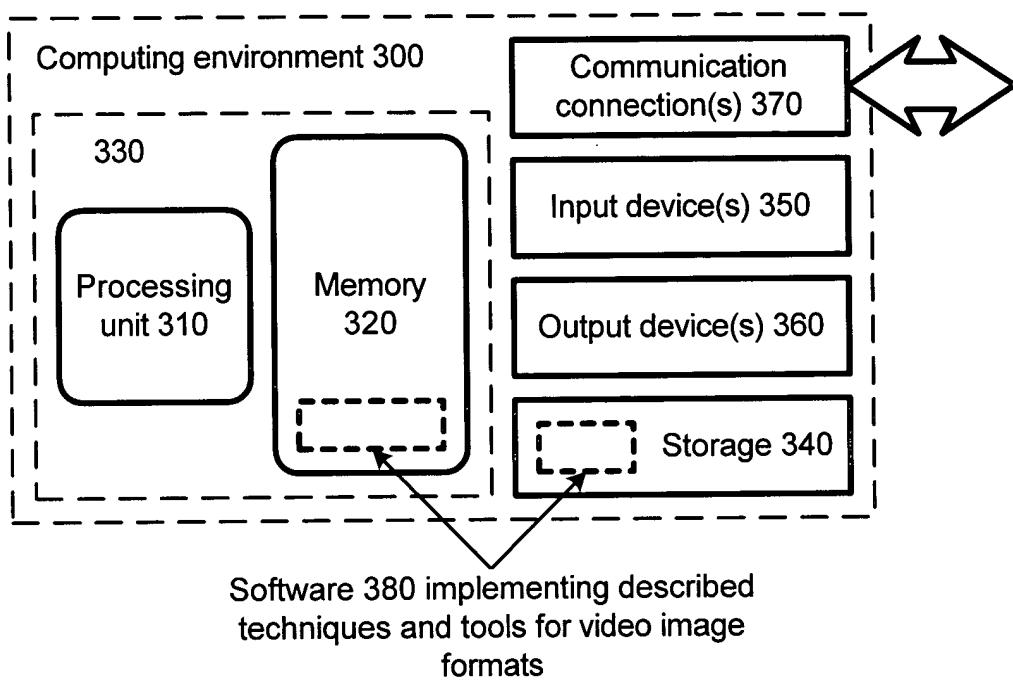


Figure 4 (Prior Art)

400
→

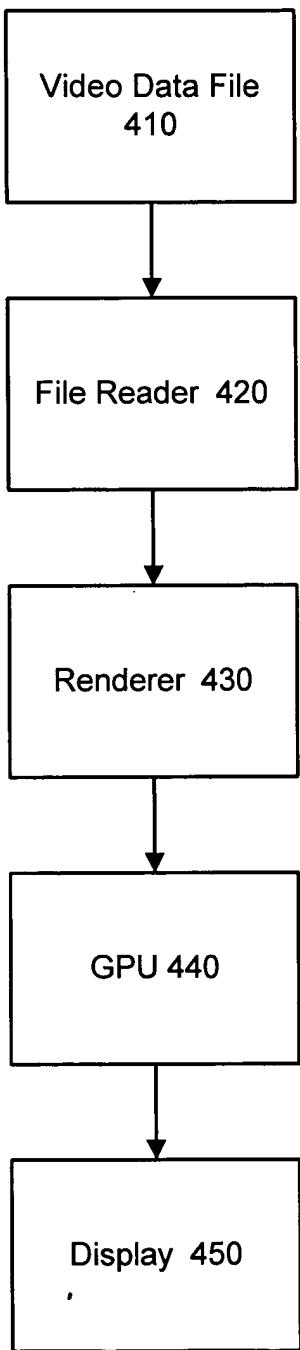


Figure 5

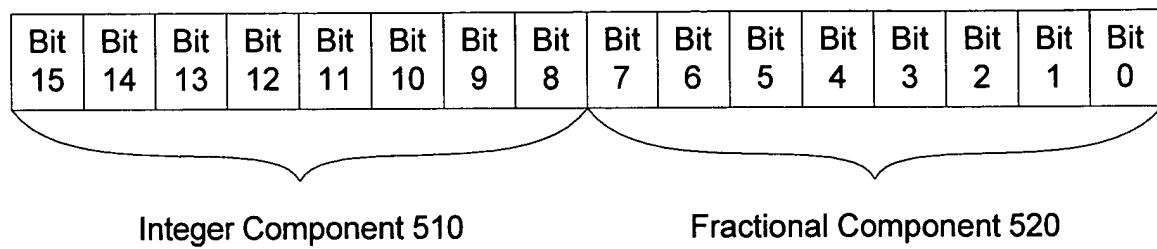


Figure 6

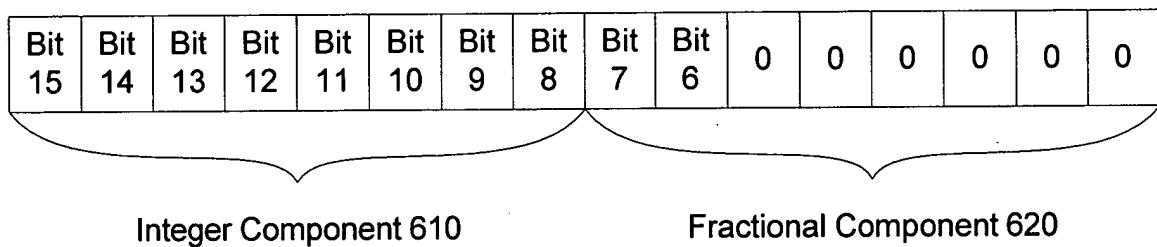


Figure 7

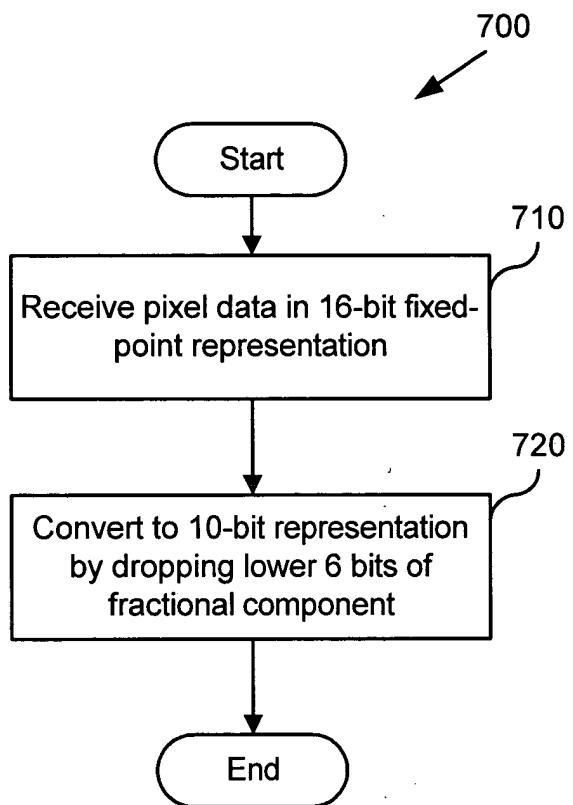


Figure 8

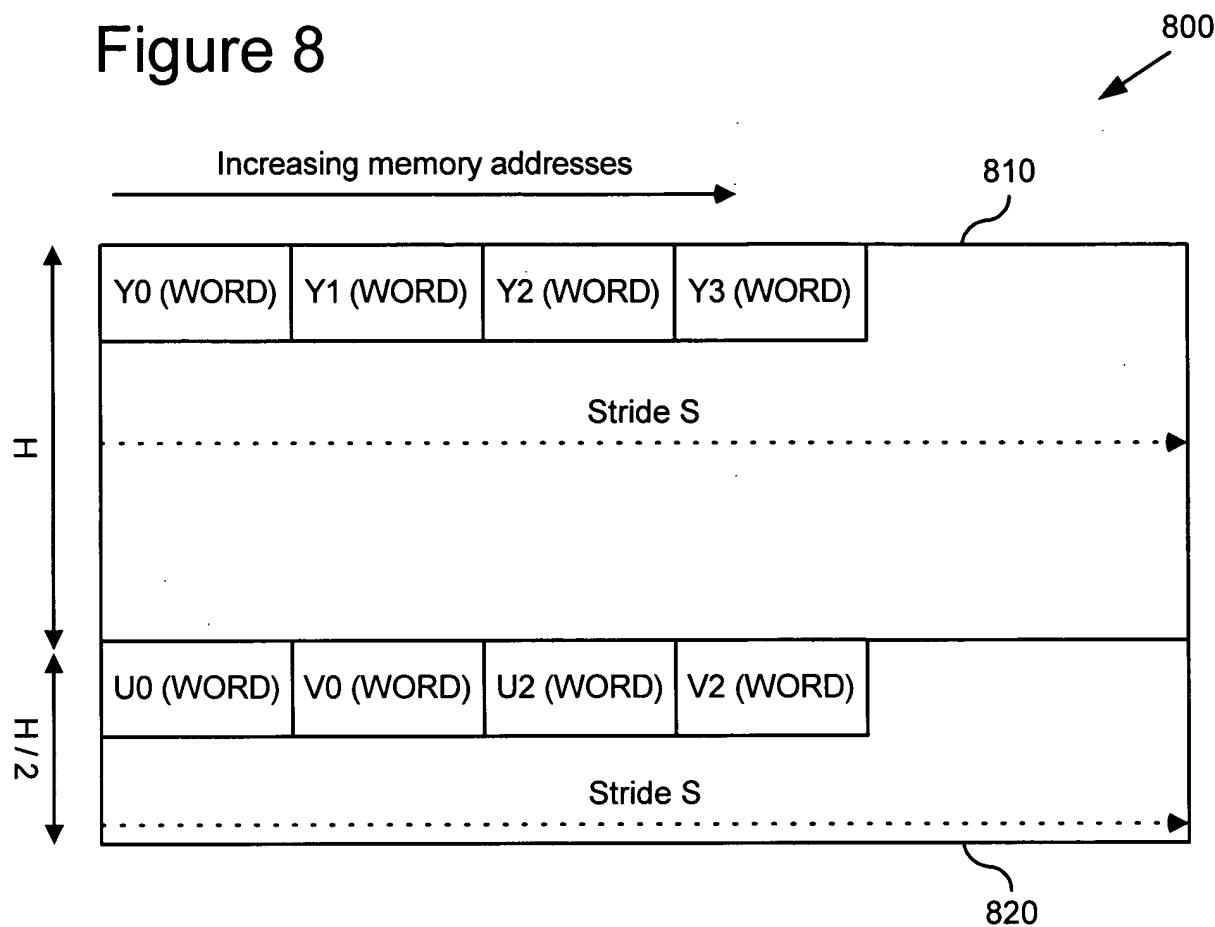


Figure 9

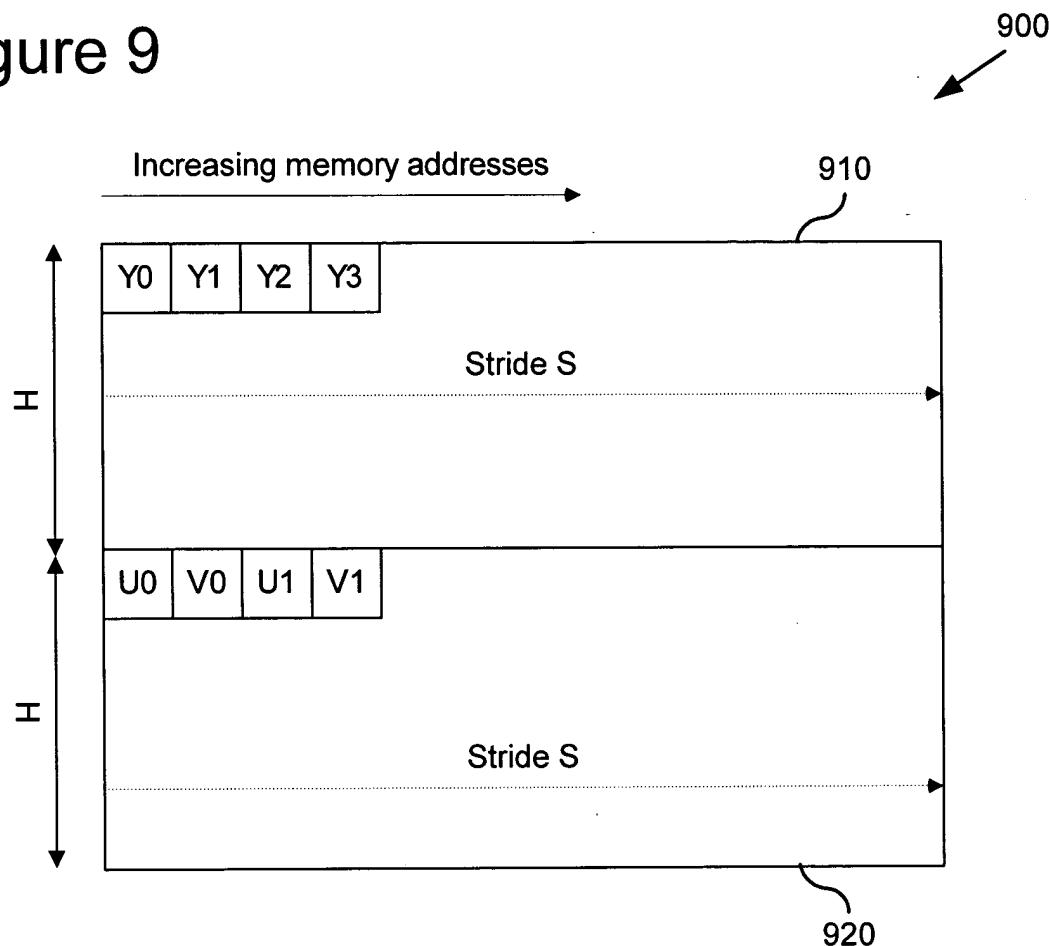


Figure 10

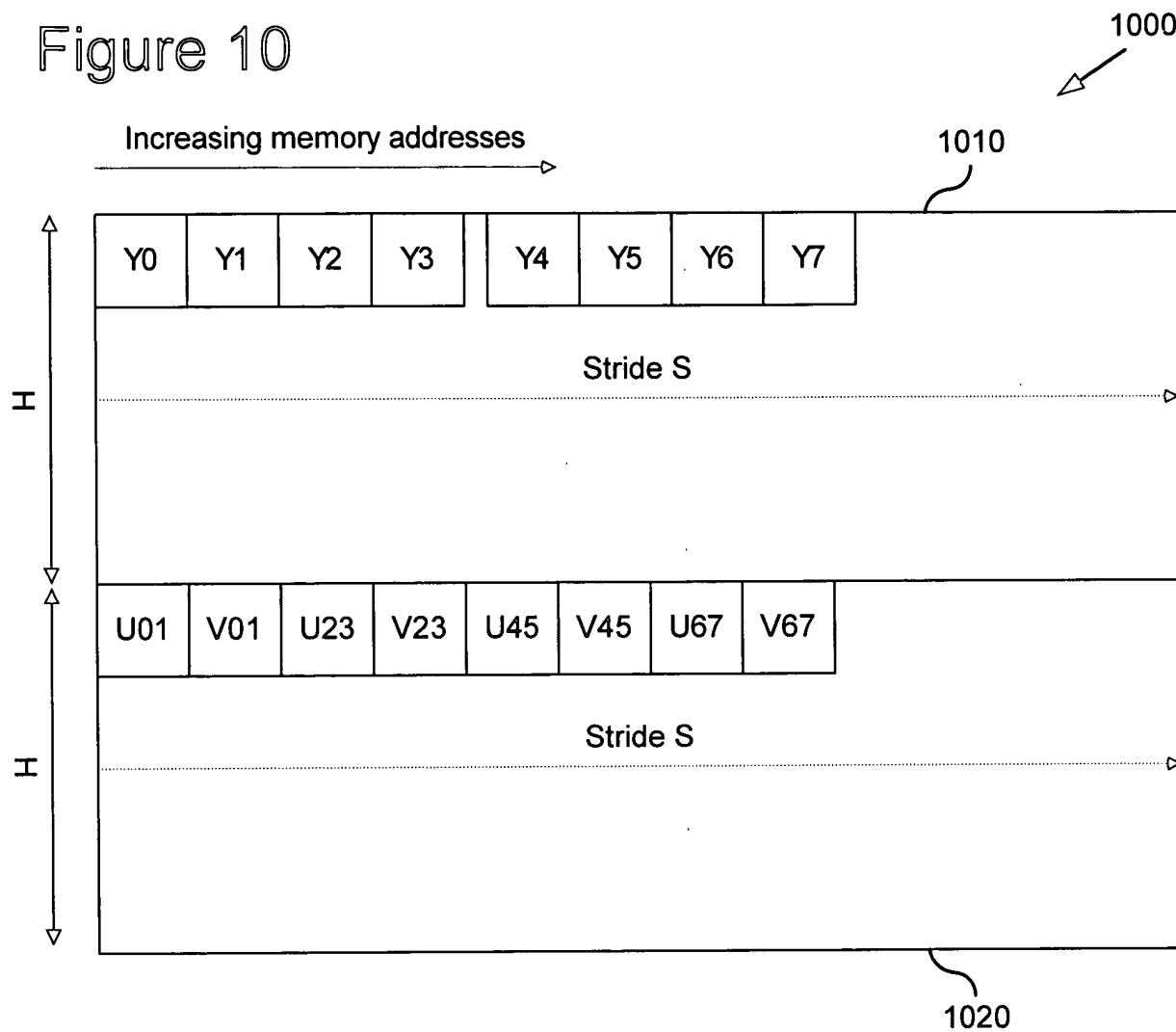


Figure 11

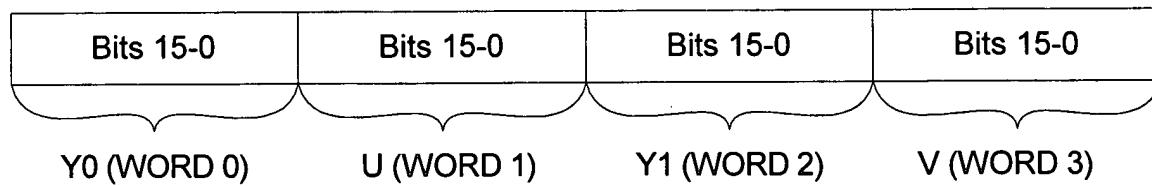


Figure 12

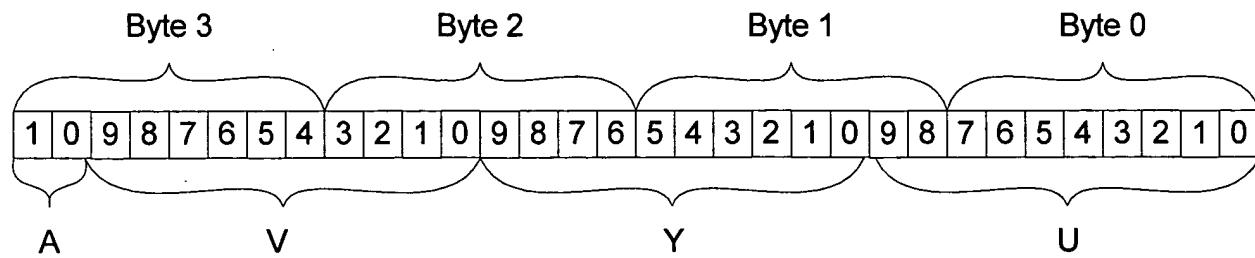


Figure 13

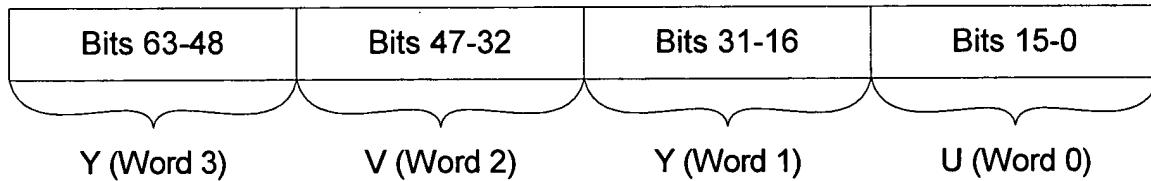


Figure 14

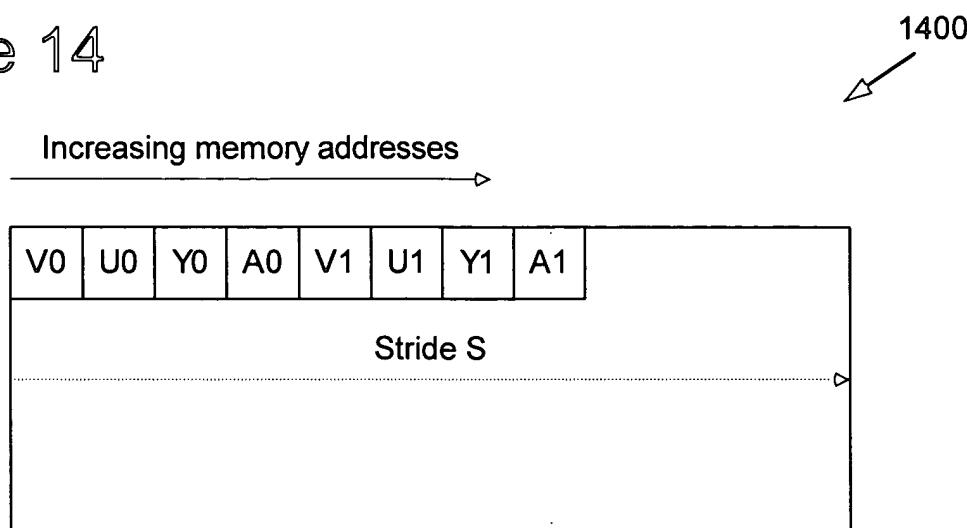


Figure 15

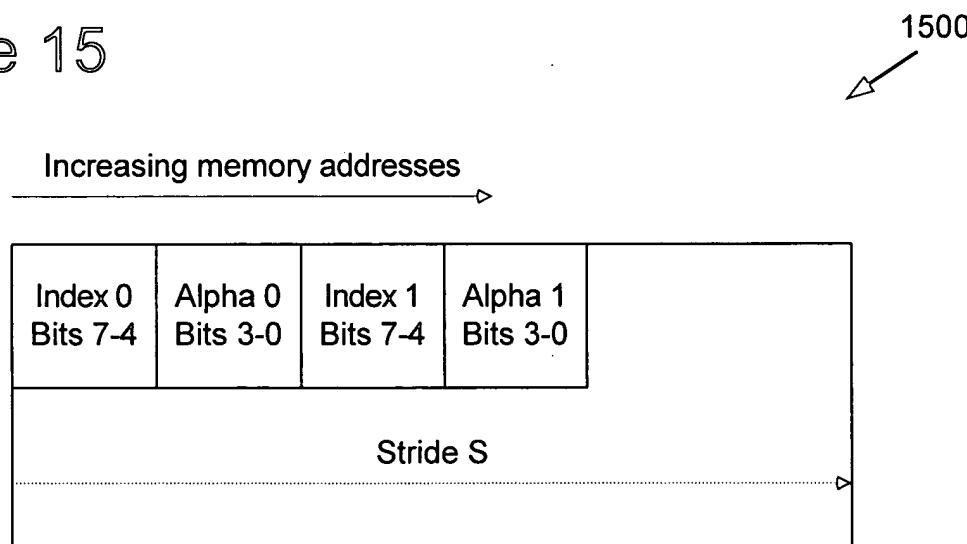


Figure 16

1600
↗

Increasing memory addresses

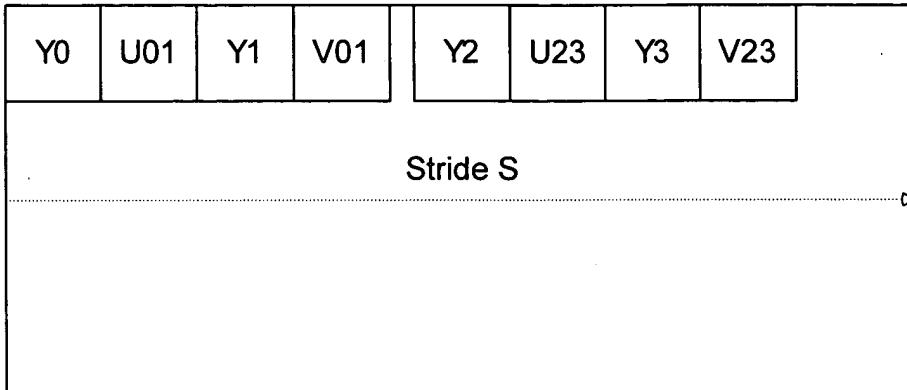


Figure 17

1700
↗

Increasing memory addresses

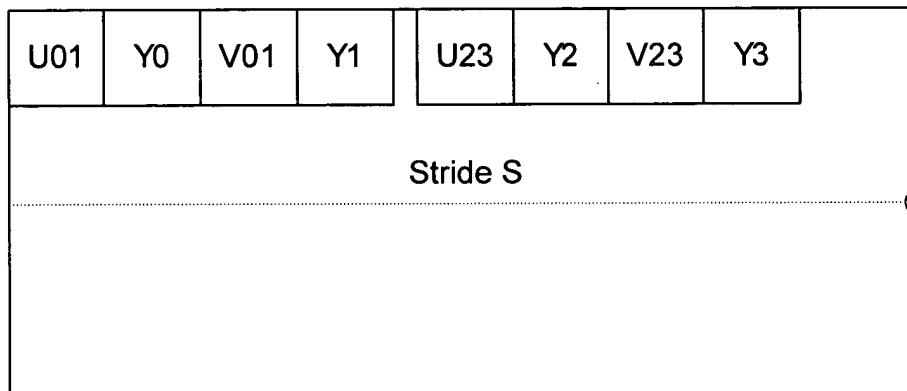


Figure 18

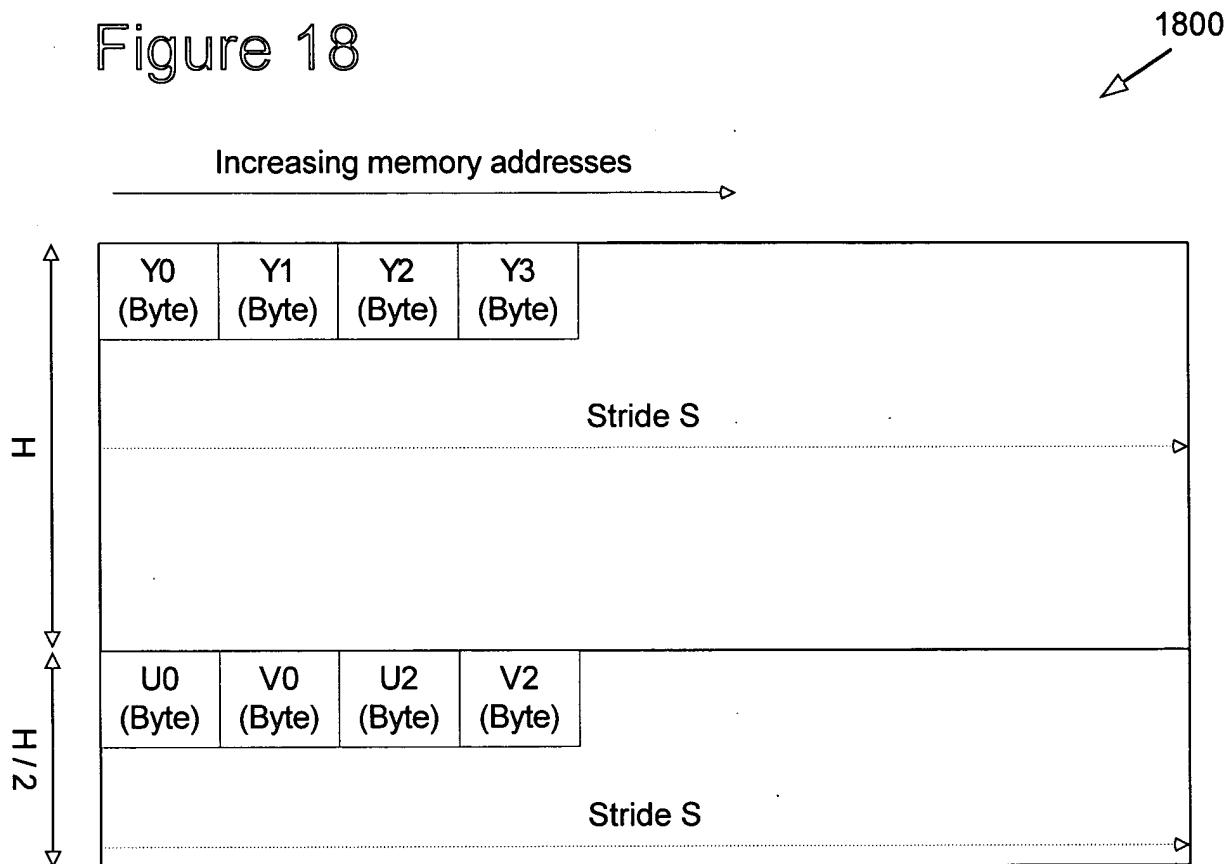


Figure 19

